

Patent Abstracts of Japan

PUBLICATION NUMBER

05181026

PUBLICATION DATE

23-07-93

APPLICATION DATE

APPLICATION NUMBER

07-01-92 04000974

APPLICANT:

SHARP CORP;

INVENTOR:

HAMADA TOSHIMASA;

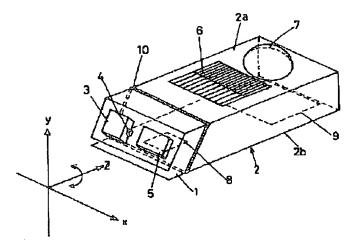
INT.CL.

G02B 6/12 G11B 7/135 G11B 7/22

TITLE

OPTICAL INTEGRATED CIRCUIT AND

ITS MANUFACTURE



ABSTRACT :

PURPOSE: To provide an optical integrated circuit capable of having high yield and greatly reducing cost by assembling a photodetector with high precision.

CONSTITUTION: A semiconductor laser 4 mounted on a submount 3 and the multi-divided photodetector 5 are assembled on a 1st transparent substrate 1. A mirror 8, a hologram beam splitter 6, a hologram collimator lens 9, and an aspherical objective lens 7 are formed integrally on the top and reverse surfaces of a 2nd transparent substrate 2. Then the 1st transparent substrate 1 is supported by an alignment adjusting means so that they can move relatively in both axial directions X and Y and rotate relatively arround a Z axis as a center to the 2nd transparent substrate 2 while holding an adhesive 10 therebetween. The light emitted the semiconductor laser 4 and reflected by an optical disk as return light is detected by the multi-divided photodetector 5 and the 1st transparent substrate 1 is aligned about the 2nd transparent substrate 2 so as to obtain a tracking servo signal and a focus servo signal; and then the adhesive 10 is cured to fix the substrates 1 and 2.

COPYRIGHT: (C)1993,JPO&Japio